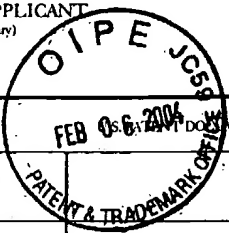


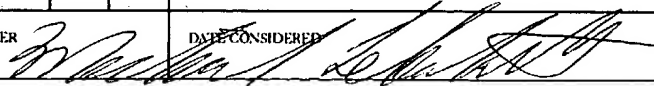
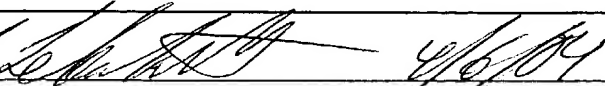
Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1599		SERIAL NO. 09/820,468	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Werner Juengling et al.			
				FILING DATE March 28, 2001		GROUP 2824	



*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
	AL					

FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AM						
	AN						
	AO						
	AP						
	AQ						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
<div style="font-size: 2em; font-family: cursive;">PAC</div>	AR		Wolf et al., "Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, pp. 428-429.
	AS		
	AT		

EXAMINER	DATE CONSIDERED
	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EL979954607